



03/14/01

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PATENT TRADEMARK OFFICE

Box: **PATENT APPLICATION**

Assistant Commissioner for Patents  
 Washington, D.C. 20231

Attorney Reference: TAIW 106

**Re:** New Patent Application of: Chun-Nan TSAI, Hou-Li CHU,  
 and Chih-Hao FENG

**Title:** PCI BUS CYCLE SINGLE-STEP INTERRUPTION DEBUG CARD

Sir:

Please find attached hereto an application for patent which includes:

- [x] Specification, Claims and Abstract (10 pages)
- [x] 3 Sheets of Formal Drawings (Fig. 1 through Fig. 3)
- [x] Inventor's Declaration and Power of Attorney (2 Pages)
- [x] Claim for priority and Priority document
- [x] Assignment document with cover page (2 Pages)
- [x] Fee (see formula below)
- [ ] Small entity form ( Page)

Basic Fee \$355/710 \$ 710.00

Additional Fees:

Total number of claims: 13 \$ 0.00  
 in excess of 20: 0 times \$9/18

Number of independent claims: 2 \$ 0.00  
 in excess of 3: 0 times \$40/80

Multiple Dependent Claims \$135/270 \$ 0.00

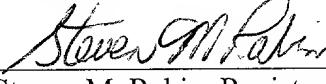
Recording Fee \$40. \$ 40.00

**TOTAL FEES FOR THE ABOVE APPLICATION** **\$ 750.00**

In the event there is attached hereto no check, or a check for an insufficient amount, please charge the fee to our Account No. 18-0002 and notify us accordingly.

The rights of priority are claimed under 35 USC §119 of Taiwanese Application No. 89105767, filed March 29, 2000.

Respectfully submitted,

  
 Steven M. Rabin, Registration No. 29,102

March 14, 2001  
 Date